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PTO/SB/21 (12/97)

Approved for use through 9/30/2000. OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	09/687,199
	Filing Date	October-12, 2000
	First Named Inventor	Stanislaw Czaja
	Group Art Unit	
	Examiner Name	
Total number of pages in this submission	Attorney Docket Number	LSI-002-PAP-2 / 00-532

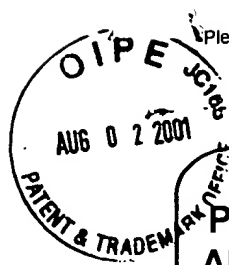
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Individual Name	Sandeep Jaggi, Reg. No. 43,331, Phone: [+1] 408-433-7191
Signature	
Date	7-31-01

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Application Number	09/687,199
Filing Date	October-12, 2000
First Named Inventor	Stanislaw Czaja
Group Art Unit	
Examiner Name	
Attorney Docket Number	LSI-002-PAP-2 / 00-532

I hereby appoint:

- ☒ Practitioners at Customer Number RECEIVED  
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RALPH VESELI	33,807
SANDEEP JAGGI	43,331
GARY B. GOATES	35,159
LEO J. PETERS	33,562
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<input checked="" type="checkbox"/> Firm or Individual Name	LSI LOGIC CORPORATION				
Address	1551 McCarthy Blvd., MS: D-106				
Address	Patent Law Department				
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Country	USA				
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I am the:

- ☐ Applicant  
☒ Assignee of Record of the entire interest  
*Certificate under 37 CFR 3.73(b) is enclose*

## SIGNATURE of Applicant or Assignee of Record

Name	SANDEEP JAGGI, Chief IP Counsel and Assistant Corporate Secretary, LSI Logic Corp.
Signature	
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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Application of :**

Stanislaw Czaja et. al.

**Serial No. :**

09/687,199

**Filed :**

October 12, 2000

**For :**

Method and Apparatus for Initiating  
a Reverse Link Intergenerational  
Handoff in a CDMA  
Communication System

**Atty Docket :** LSI-002-PAP-2 / 00-532

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington D.C. 20231, on the date indicated below:

Connie Del Castillo

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## CERTIFICATION UNDER 37 C.F.R. 3.73(b)

Assistant Commissioner for Patents  
Washington, D.C. 20231

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Sir:

LSI Logic Corporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

LSI Logic Corporation  
1551 McCarthy Blvd., MS D-106  
Milipitas, CA 95035  
(408) 433-8708

Date: 7-31-01

Respectfully submitted,

*[Signature]*  
Sandeep Jaggi  
Chief IP Counsel  
Corporate Assistant Secretary  
LSI LOGIC CORPORATION